



BSI Standards Publication

Test methods for electrical materials, printed boards and other interconnection structures and assemblies

Part 5-2: General test methods for materials and assemblies — Soldering flux for printed board assemblies

National foreword

This British Standard is the UK implementation of EN 61189-5-2:2015. It is identical to IEC 61189-5-2:2015. It supersedes BS IEC 61189-5-2:2015, which is withdrawn.

The UK participation in its preparation was entrusted to Technical Committee EPL/501, Electronic Assembly Technology.

A list of organizations represented on this committee can be obtained on request to its secretary.

This publication does not purport to include all the necessary provisions of a contract. Users are responsible for its correct application.

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Amendments/corrigenda issued since publication

Date	Text affected
30 April 2015	This corrigendum renumbers BS IEC 61189-5-2:2015 as BS EN 61189-5-2:2015

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English Version

Test methods for electrical materials, printed boards and other interconnection structures and assemblies - Part 5-2: General test methods for materials and assemblies - Soldering flux for printed board assemblies
(IEC 61189-5-2:2015)

Méthodes d'essai pour les matériaux électriques, les cartes imprimées et autres structures d'interconnexion et ensembles - Partie 5-2: Méthodes d'essai générales pour les matériaux et les assemblages - Flux de brasage pour les assemblages de cartes imprimées
(IEC 61189-5-2:2015)

Prüfverfahren für Elektromaterialien, Verbindungsstrukturen und Baugruppen - Teil 5-2: Prüfverfahren für bestückte Leiterplatten - Teil Lötflussmittel
(IEC 61189-5-2:2015)

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This European Standard exists in three official versions (English, French, German). A version in any other language made by translation under the responsibility of a CENELEC member into its own language and notified to the CEN-CENELEC Management Centre has the same status as the official versions.

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